

## FPGA Engineer

**Location:** Bengaluru (Client Site)

**Job Type:** Full-time

**Experience:** 3-7 years

**Notice Period:** 0-15 days (immediate joiners preferred)

**No. of Positions:** 1 Lead & 4 Engineers

### About the Role:

Advantal Technologies Limited is seeking a skilled FPGA Engineer with 3–7 years of experience in RTL design using Verilog, along with expertise in Xilinx MPSoC platforms, MicroBlaze processor development, and embedded system security aspects such as authentication, encryption/decryption, and certificates. The ideal candidate will play a key role in architecting and implementing secure, high-performance digital logic systems.

### Requirement:

- Experience band 3-7 years
- Experience in RTL coding using Verilog
- Experience on development on Xilinx MPSoC (preferably ZCU 106/104)
- Hands-on experience with Xilinx Vivado and Vitis
- Desirable to have experience with MISRA C coding guidelines
- Desirable to have experience with DO-254
- Desirable to have experience with Microblaze
- Desirable to have experience in security aspects of authentication, certificates, encryption/decryption

### How to Apply:

If you are passionate about embedded systems and meet the above requirements, we would love to hear from you.

**Kindly share your resume at: [hr@advantal.net](mailto:hr@advantal.net)**

**For more information, connect with us at: +91 91312 95441**